

Fig. 1

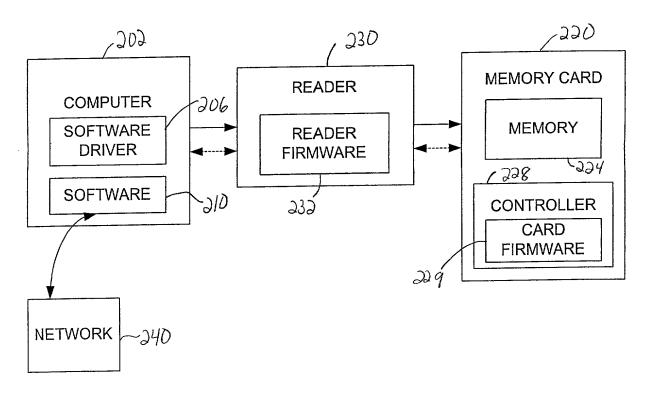
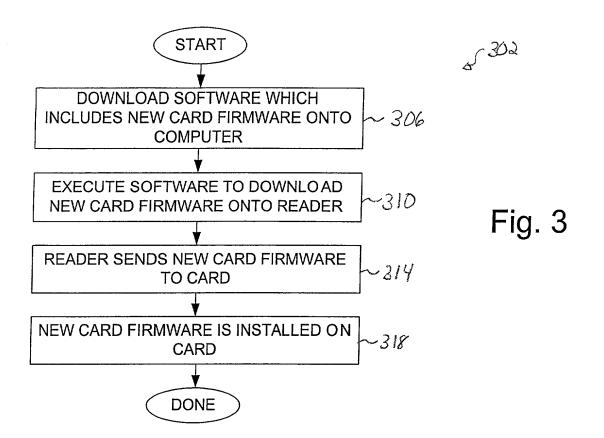
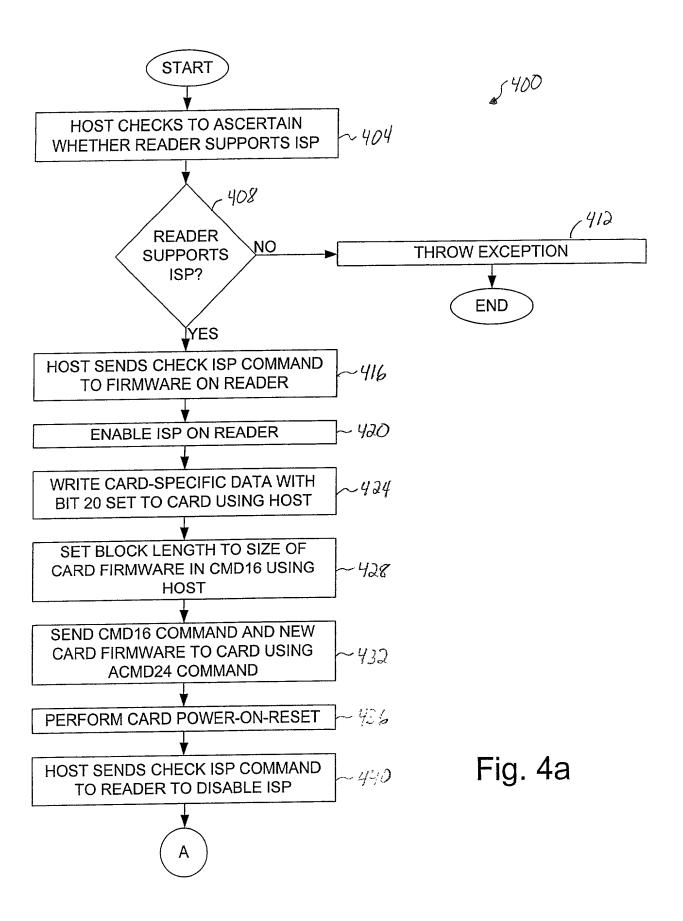


Fig. 2





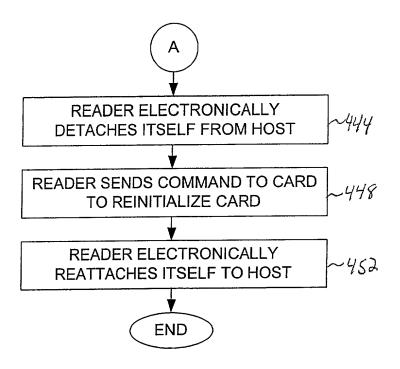
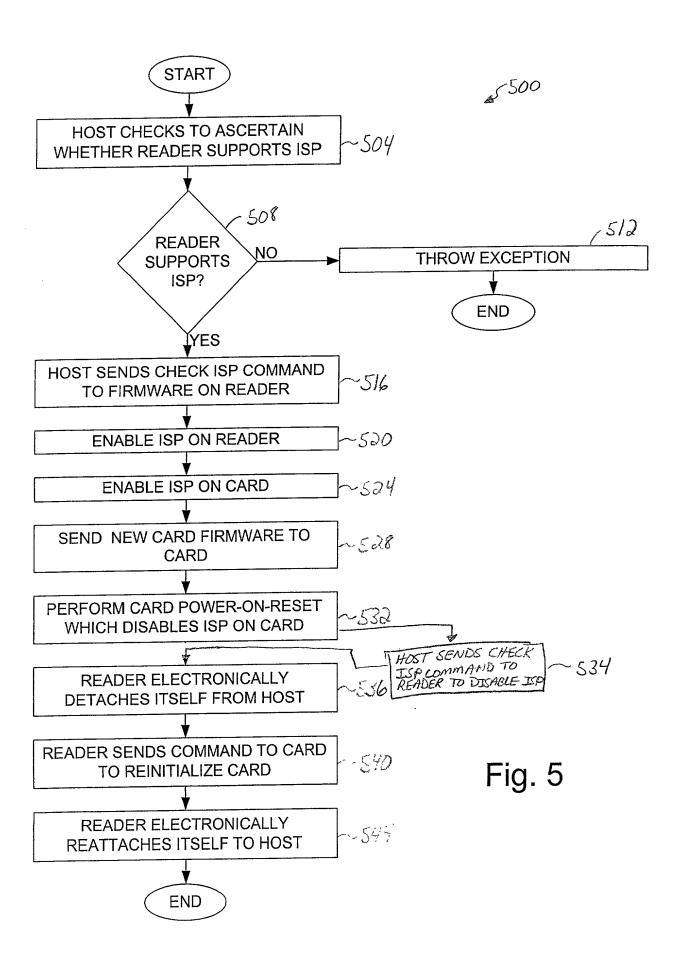


Fig. 4b



Ī	Bit 7	610h 610e	9 / 6! 5	610e	3	<u>)d 7610</u> 2)c /6/1	0 0			
Byte		0	J	7							
0			Op	eration Co	de (D0t	າ)					
1		Reserved									
2		Reserved									
3		Reserved									
4		Reserved									
5		Reserved									
6				Reserve	d						
7				Reserve	d						
8				Reserve	d						
9				Reserve	d						
10	Lock	nck Reserved ISP PA ENB EN									
. 11		Reserved									

4 602

Fig. 6a

									66	22	
			6301	630	g -63	OF ,63	cė, 63	°08,6	30c p	630b , s	30a
	В	it	7	6	5	4	3	2	1	0	
	Byte		,	•							
(Odba	0		Rese	erved		Media C	ard Type	9	ISP ENB	PASS ENB	
	1				F	RCA Bits	31-24				
	2 ,				F	RCA Bits	23-16				
	626c 626b	-									

Fig. 6b

		1660	h /660	g 1660F	/660e	<i> </i> 6608	1660c	16606	166				
D. 44	Bit	7	6	5	4	3	2	1	0				
Byte	9		Operation Code (D1h)										
6560 0			Operation Code (D1h)										
656 1	_	-	Reserved										
65/2			Command Index										
1 3			Command Argument 1										
556 4			Command Argument 2										
5				Comr	nand Arg	gument 3	3						
560 6				Comr	nand Arg	gument 4							
1/7				Data Tra	ansfer Le	ngth (M	LSB)						
65g 1 656; 8				Data Tra	nsfer Le	ngth (LN	/ISB)						
9				Data Tra	ansfer Le	ength (LL	SB)						
, 10			Response Type DIR APP RESP RESERVED										
11	, 1		Control										
656K	656K 6561												

₹ 652

Fig. 6c

		171	09 /710	F /710	-710e	7108	17100	7710	b /7100				
	Bit	7	6	5	4	3	2	1	0				
	Byte				•		_						
7060	0			Ор	eration C	ode (D2	h)						
1	1		Reserved										
7065	2		'S'										
7066	3		'D' Reserved (Error Register)										
7068	4												
706e	5		Secto	r Count	Register	/Feature	Register	•					
706f	6			S	ector Reg	gister							
ANG	7			Cylii	nder Low	Registe	r						
砌	8			Cylir	nder High	Registe	r						
706i	9			Dev	ice/Head	Registe	٢						
705	_10			Comm	and/Stati	us Regis	ter						
100	11 ,		Reserved DATA DIR REGS										
(,	706K 700												
	106K 706f							A					
		Fig. 7a											

Fig. 7a

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									,					
	Bit	7	6	5	4	3	2	1	0					
Byte														
			'S'											
$\frac{1}{2}$			'D' Reserved (Error Register)											
7/6/ 2														
Hbe 3			Sector Count Register/Feature Register											
716, 4			Sector Register											
7/69 5				Cyli	nder Low	Registe	r							
6				Cylin	nder High	Registe	er							
76K/ 7				Dev	ice/Head	Registe	r							
8,			Command/Status Register											
7161	7/61.													
7168	11~3													

Fig. 7b

The state of the s

		1808h	,8089	, 808F	,80Se	,8081	1808c	18086	18080		
	Register	٠ 7	6	έ5	4	′ 3	2	<u> </u>	0		
8126	Feature				na				ENB		
803	Sector Count				na						
812	Sector Number				na						
8121	Cylinder Low	na									
81-6	Cylinder High				na						
5,5	Device/Head	obs	na	Obs	DEV		n	a			
	Command ,	, ,	-		DSh !						
1,-	812F 812g										
	29							£804			

Fig. 8a

814~

		1818h	18189	,818f	,818e	8188	,818c	,8186	,8180			
	Register	[*] 7	6	໌ 5	4	΄ 3	٤ 2	1 1	0			
822a	Error				0h	l						
8939	Sector Count				55I	1						
(04)	Sector Number				AA	h						
EN SOUTH	Cylinder Low		0h									
80-de	Cylinder High	0h										
00°C	/ Device/Head	obs	na	Obs	DEV	WP	M	ledia Typ	е			
1	Status ,	BSY	DRDY	DF	1	DRQ	0	0	ERR			
1	827 822a					·						

Fig. 8b

		12	28h 186	ta 188	4 1808e	, 828	1898	182	86,8280			
	Register	7	6	5	4	3	2	1	0			
	Error	0	0	0	0	0	ABRT	0	0			
8824	Sector Count		na									
8333	Sector Number		na									
832	Cylinder Low		na									
833	Cylinder High				na							
Be	, Device/Head	obs	na	Obs	DEV		n	а				
	Status ,	BSY	DRDY	DF	na	DRQ	na	na	ERR			
L	830F 830g						4	₹ ₈₂₄				

Fig. 8c

							d	1844				
		1848h	, 848g	18487	1848e	8481	18486	₁ 848b	1848			
[Register	7	6	5	4	3	2	1	0			
85Ja	Feature				0h				DIR			
852P	Sector Count	Number of sectors										
526	Sector Number			Starting	LBA of F	irmware	(LSB)					
823)	Cylinder Low		Sta	rting LBA	of Firm	vare (Mi	ddle Byt	e)				
1	Cylinder High			Starting I	_BA of Fi	rmware	(MSB)					
Sie-	, Device/Head	obs	na	obs	DEV		0	h				
	Command D6h											
	852f 852g											

Fig. 8d

		1868h	1868g	(8684	(868e	18681	18680	1868	6 18680		
	Register	7	6	5	4	3	2	1	0		
(2)	Error				na						
872a	Sector Count				na						
872	Sector Number				na						
8721	Cylinder Low	na									
8720	Cylinder High	na									
8 120	/ Device/Head	obs	na	Obs	DEV		n	a			
	Status	BSY	DRDY	DF	na	DRQ	na	na	ERR		
_	8725 8729	ì									

Fig. 8e

	88	4-20									
		18881	18889	1888F	(888e	1886	1 888c	1 888	18880		
	Register	7	6	5	4	3	2	1	0		
(02)	Error	na	UNC	ñai	na	nâ	ABRT	na	na		
897a	Sector Count				na						
893	Sector Number	Error LBA (LSB)									
892	Cylinder Low			Erro	r LBA (M	liddle By	te)				
894	Cylinder High			Е	rror LBA	(MSB)					
892	_ Device/Head	obs	na	obs	DEV		0	h			
/	STATUS /	BSY	DRDY	DF	na	DRQ	na	na	ERR		
	89) , 89).	3									

Fig. 8f